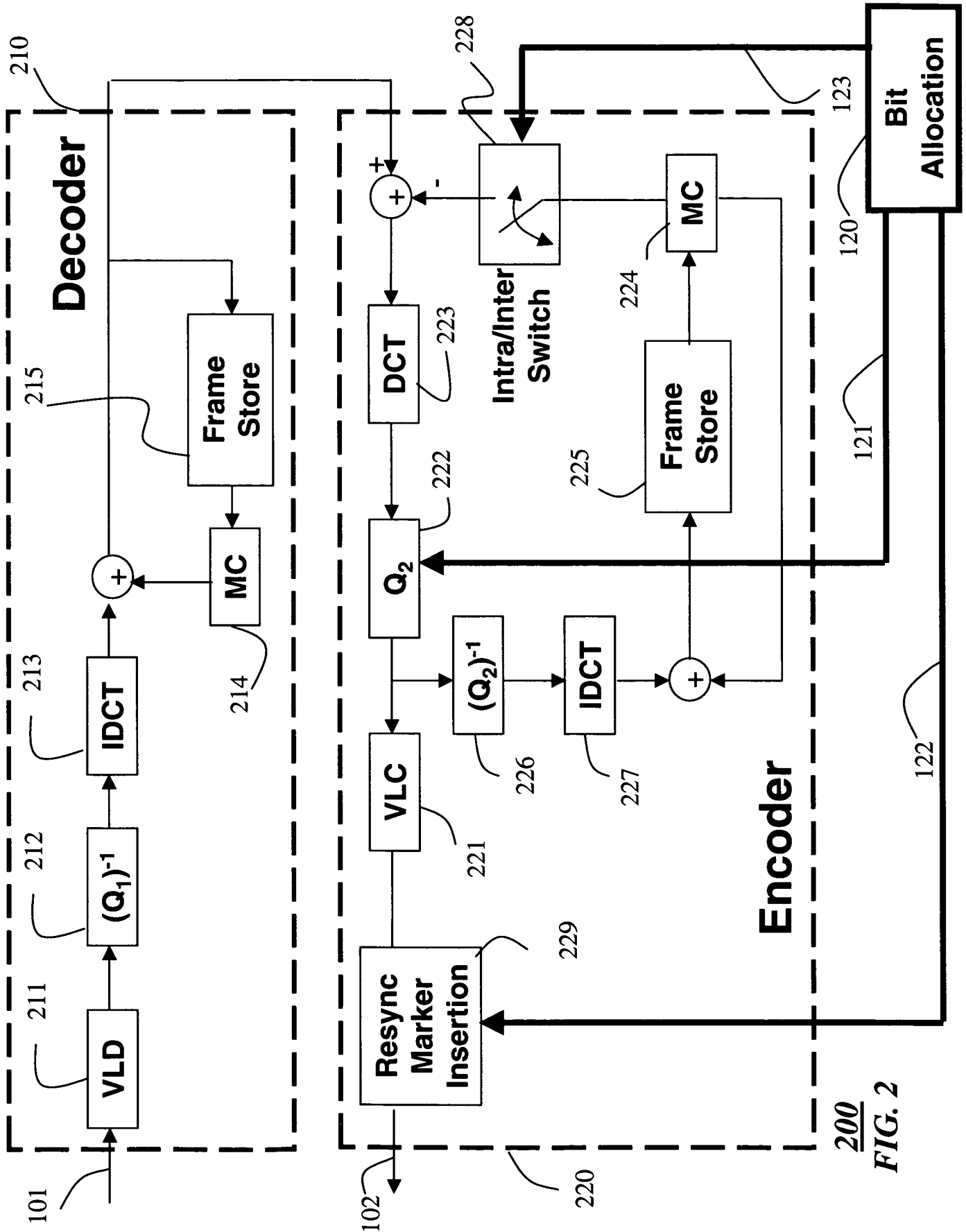
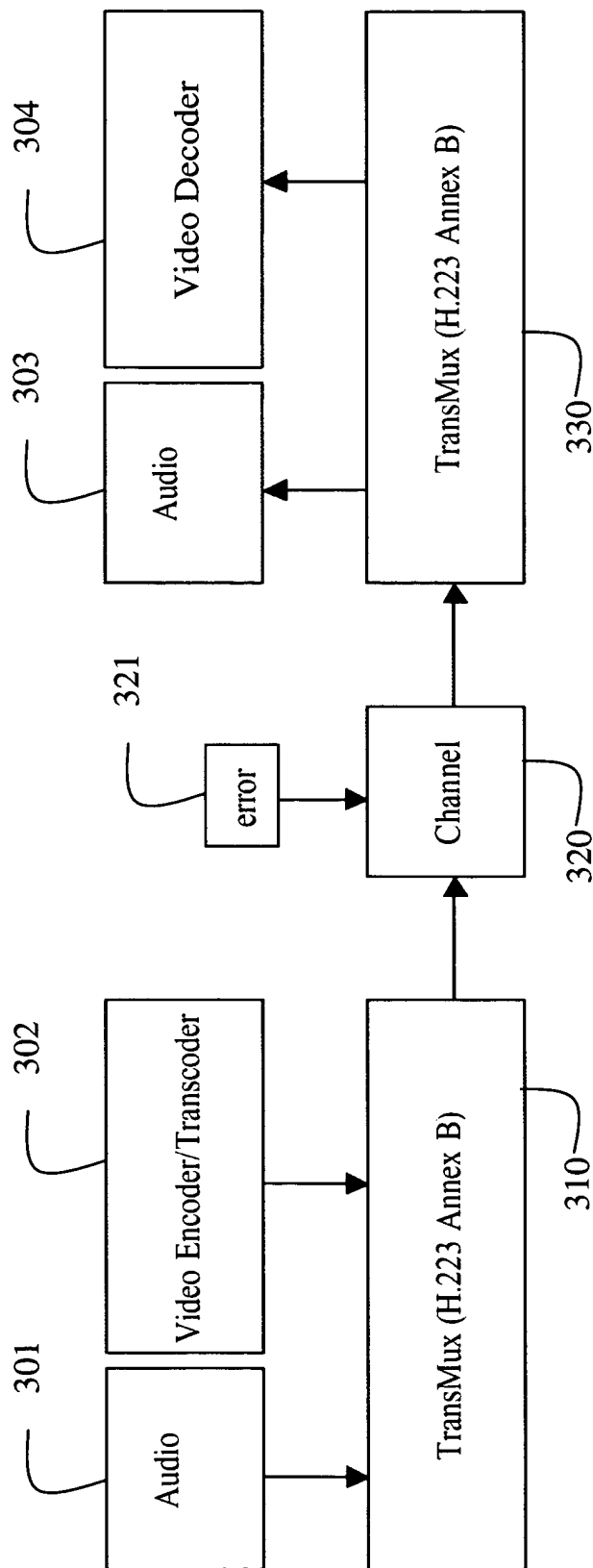


100

FIG. 1



200
 FIG. 2



300
FIG. 3

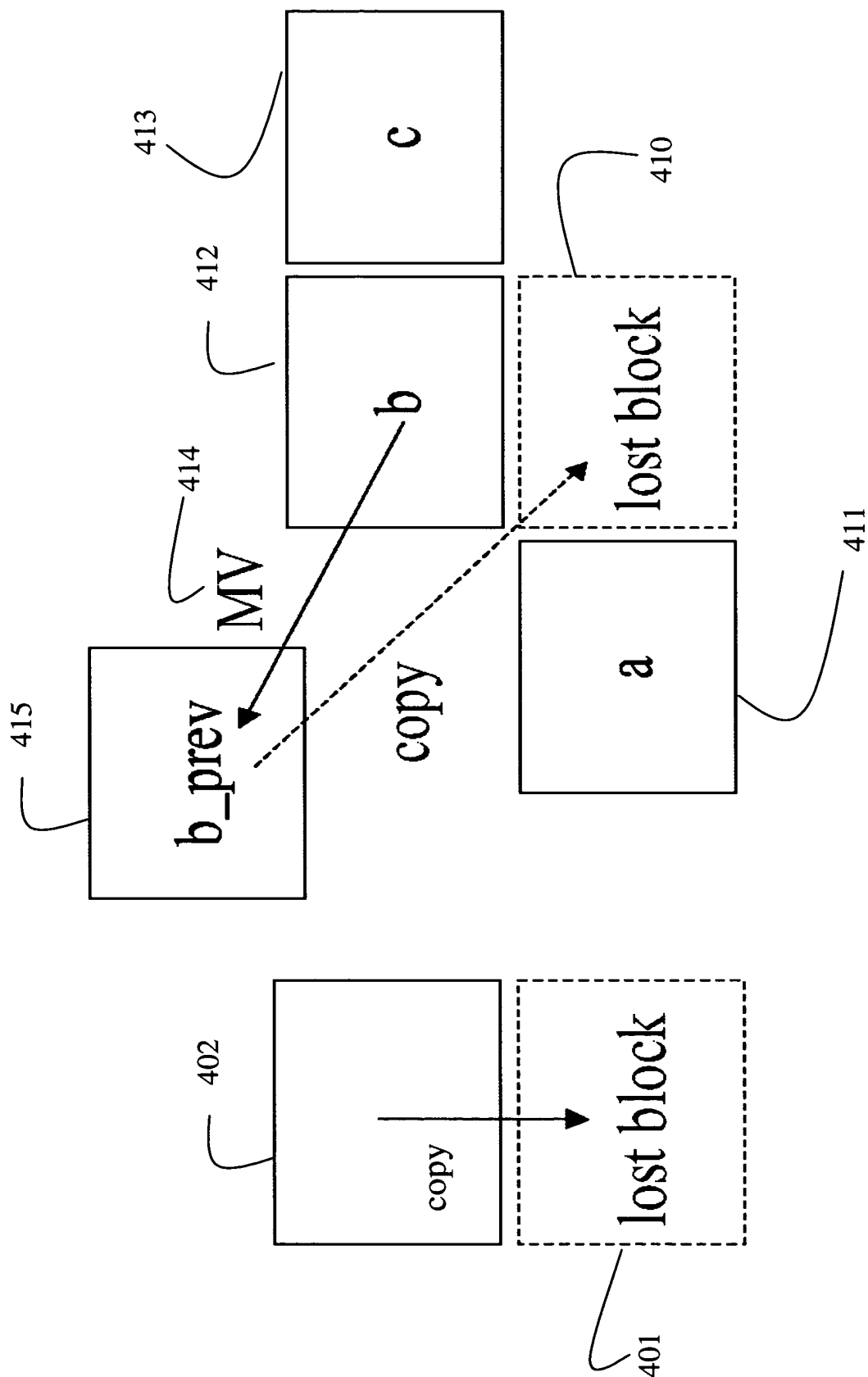


FIG. 4

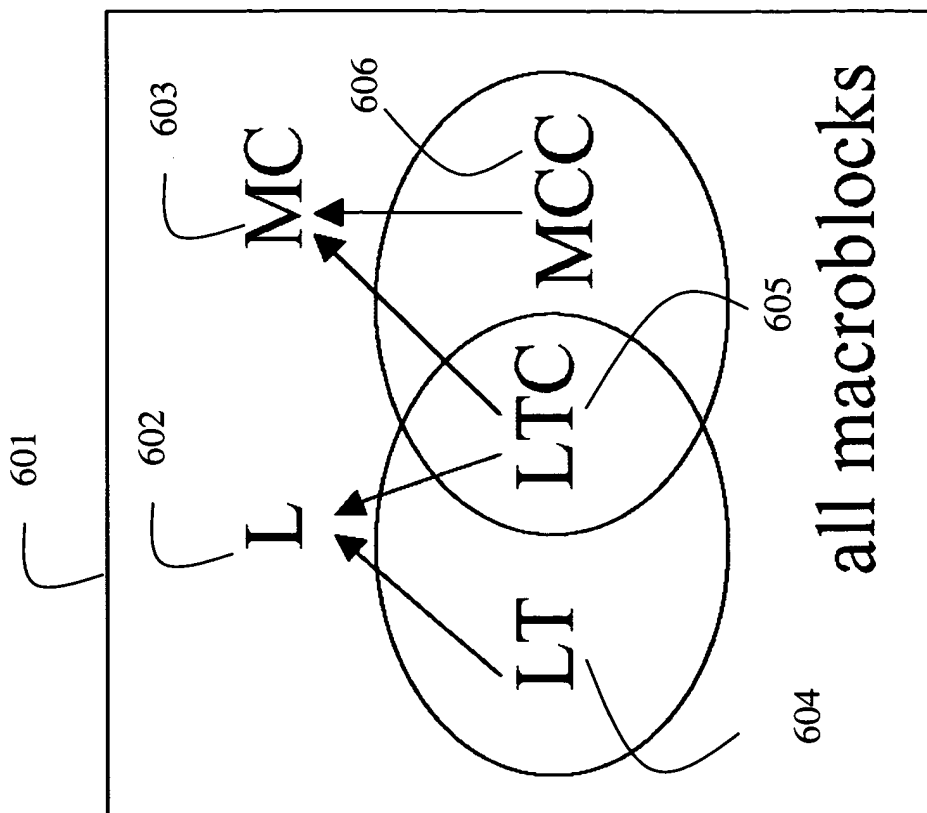


FIG. 6

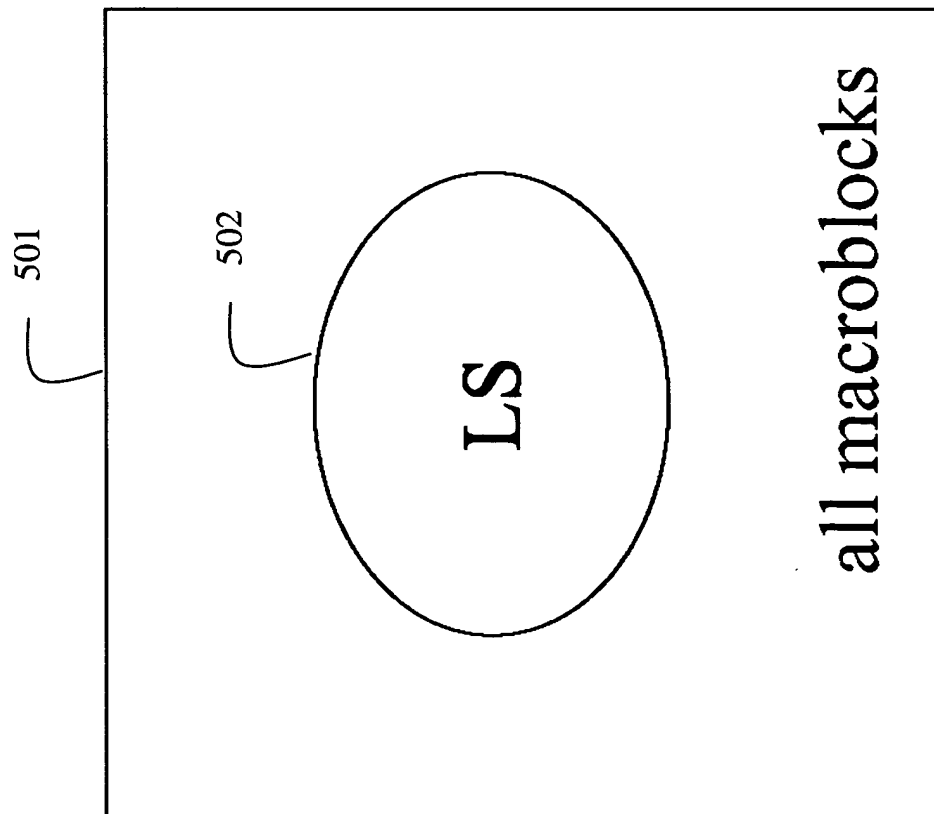


FIG. 5

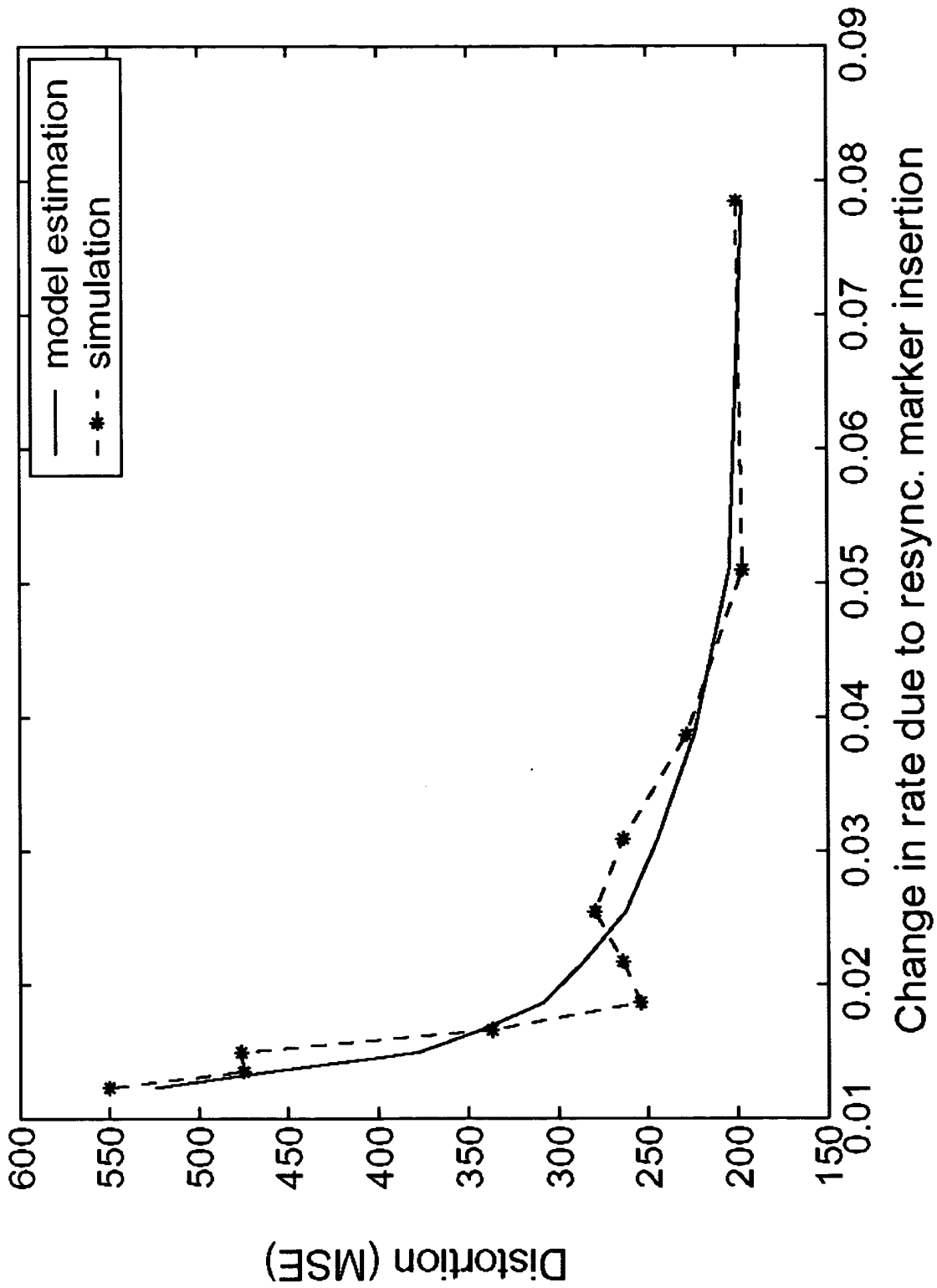


FIG. 7

Optimal Bit Allocation for Error Resilient Video Transcoding

Inventor(s): Vetro, et al.
Filed: February 11, 2004

Attny. Docket: MERL-1544
7 of 7 Drawing Sheets

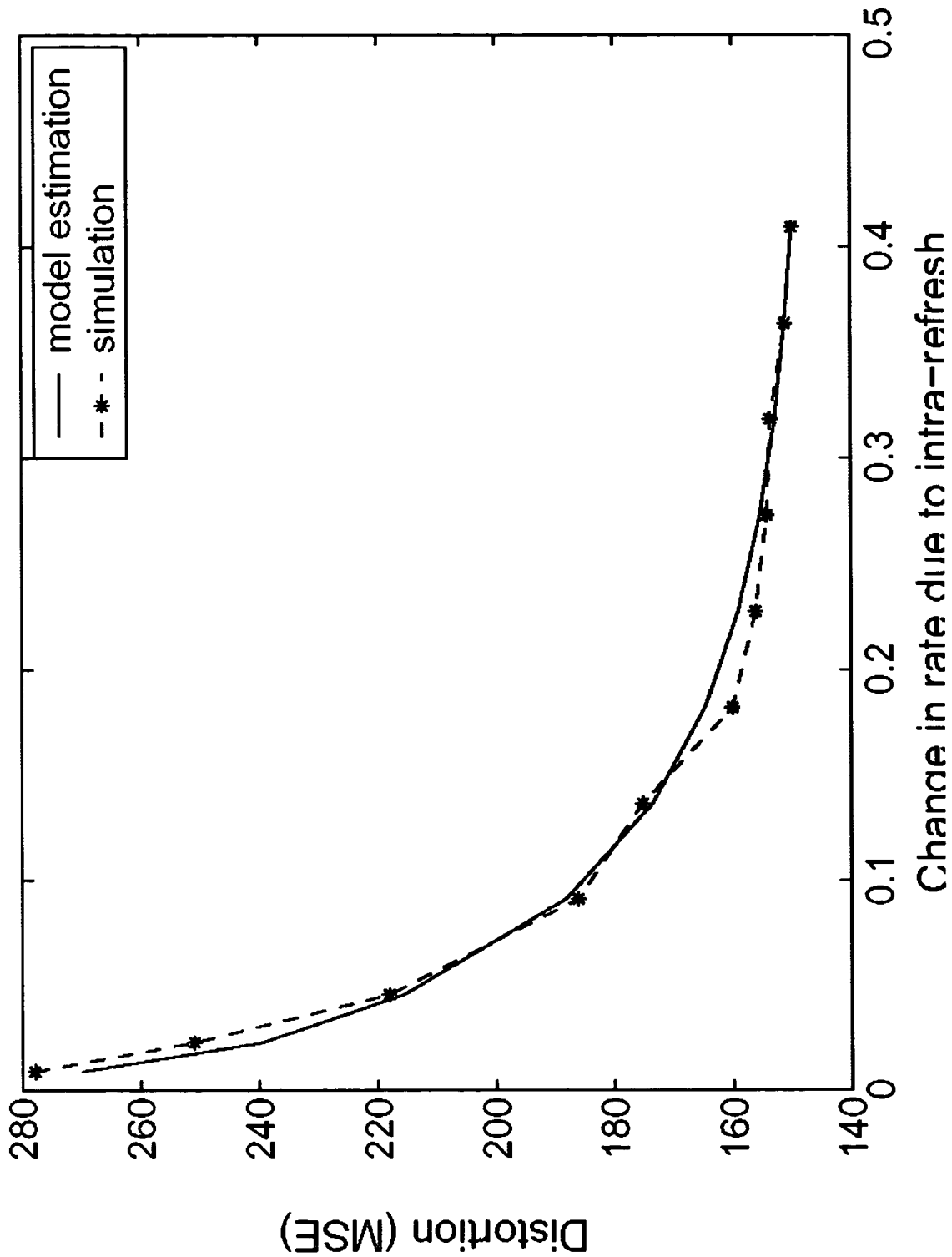


FIG. 8